

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:	Hideaki SAKAGUCHI	Conf.:	4601
Appl. No.:	09/624,014	Group:	2857
Filed:	July 21, 2000	Examiner:	Mary C. BARAN
For:	TESTING DEVICE & TESTING METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUITS		

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

July 23, 2003

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

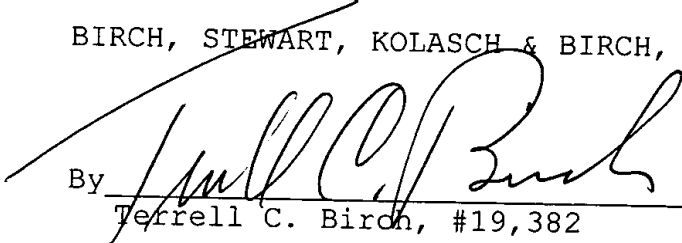
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	11	-	20	=	0	\$18	\$0.00
INDEPENDENT	4	-	4	=	0	\$84	\$0.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$0.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Terrell C. Birch, #19,382

TCB/RWD/kss
1152-0263P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

Attachment(s)

(Rev. 10/15/02)